30 PIN SIMM at the compute	er.	
----------------------------	-----	--

Pin	Name	Description
1	VCC	+5 VDC
2	/CAS	Column Address Strobe
3	DQ0	Data 0
4	A0	Address 0
5	A1	Address 1
6	DQ1	Data 1
7	A2	Address 2
8	A3	Address 3
9	GND	Ground
10	DQ2	Data 2
11	A4	Address 4
12	A5	Address 5
13	DQ3	Data 3
14	A6	Address 6
15	A7	Address 7
16	DQ4	Data 4
17	A8	Address 8
18	A9	Address 9
19	A10	Address 10
20	DQ5	Data 5
21	/WE	Write Enable
22	GND	Ground
23	DQ6	Data 6
24	A11	Address 11
25	DQ7	Data 7
26	QP	Data Parity Out
27	/RAS	Row Address Strobe
28	/CASP	Something Parity ????
29	DP	Data Parity In
30	VCC	+5 VDC

Here is the pinout of the IIgs memory expansion slot

1	GND
2	+5V
3	FRA9
4	FRA8
5	D2
6	FRA6
7	FRA3
8	FRA4
9	FRA5
10	FRA7
11	+5V
12	FR/W
13	FRA0
14	FRA2
15	FRA1
16	D7
17	/CCAS
18	CROW0
19	CROW1
20	/CROMSEL
21	+5V
22	GND
23	GND
24	+5V
25	D0
26	/CSEL
27	MSIZE
28	D6
29	D4
30	D5
31	PHI2
32	ABORT
33	D3
34	GND
35	/CRAS
36	D1
37	A10
38	A11
39	A12
40	A13
41	A14
42	A15
43	+5V
12712	The same

GND